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Serial Number: 10801364

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## PALM INTRANET

Day: Friday Date: 10/20/2006 Time: 13:41:43

## **Inventor Information for 10/801364**

| Inventor Name                        | City                                   | State/Country  |
|--------------------------------------|--|--|
| HUANG, QINGFA                        | XIAMEN                                 | CHINA  |
| LIN, FENGXI                          | XIAMEN .                               | CHINA  |
| Appln Info Contents Petition Info    |  | Foreign Data Invent  |
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| Attorney Docket #                    | Search                                 | and the same of th |
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Back to PALM | ASSIGNMENT | OASIS | Home page

| US<br>20060231674<br>A1 | 20061019 | Movable lifting stand  | 242/615.2 |  | Lin; Feng-<br>Hsu                      |
|-------------------------|----------|--|-----------|--|--|
| US<br>20060228845<br>A1 | 20061012 | Method for pre-<br>retaining CB opening  | 438/197   | ·  | Chen;<br>Yinan et<br>al.               |
| US<br>20060216349<br>A1 | 20060928 | IBUPROFEN<br>COMPOSITION   | 424/464   | 424/440;<br>514/570                            | Bunick;<br>Frank J. et<br>al.          |
| US<br>20060203948<br>A1 | 20060914 | Phase detector and<br>method providing rapid<br>locking of delay-lock<br>loops                       | 375/375   | 375/376  | Lin; Feng                              |
| US<br>20060202726<br>A1 | 20060914 | Fast-locking digital phase locked loop   | 327/141   |  | Lin; Feng et al.                       |
| US<br>20060198237<br>A1 | 20060907 | Method and apparatus<br>for initialization of read<br>latency tracking circuit<br>in high-speed DRAM | 365/233   |  | Johnson;<br>James .<br>Brian et<br>al. |
| US<br>20060195713<br>A1 | 20060831 | Duty cycle distortion compensation for the data output of a memory device                            | 713/400   |  | Johnson;<br>James B.<br>et al.         |
| US<br>20060170637<br>A1 | 20060803 | LCD panel and LCD device   | 345/96    |  | Lin; Feng-<br>Li                       |
| US<br>20060161716<br>A1 | 20060720 | Adapter for connecting a portable memory unit to a host, and a memory device having the adapter      | 710/313   |  | Lin; Feng<br>Hsi                       |
| US<br>20060143491<br>A1 | 20060629 | Memory system and method for strobing data, command and address signals                              | 713/600   | 365/189.05;<br>365/233;<br>713/400;<br>713/500 | Lin; Feng<br>et al.                    |
| US<br>20060140023<br>A1 | 20060629 | Memory system and<br>method for strobing<br>data, command and<br>address signals                     | 365/193   |  | Lin; Feng et al.                       |
| US<br>20060133165<br>A1 | 20060622 | Memory system and method for strobing data, command and address signals                              | 365/193   |  | Lin; Feng et al.                       |
| US<br>20060127764       | 20060615 | Electrochemical cells electrodes and binder  | 429/217   | 429/206  | Chen;<br>Muguo et                      |

41

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| Al          |          | materials for                             |           |            | al.                |
|-------------|----------|---|-----------|------------|--------------------|
|             |          | electrochemical cells                     |           |            |                    |
|             |          | electrodes                                |           |            |                    |
| US          | 20060615 | Memory system and                         | 365/193   |            | Lin; Feng          |
| 20060126406 |          | method for strobing                       |           |            | et al.             |
| A1          |          | data, command and                         |           |            |                    |
|             |          | address signals                           |           |            |                    |
| US          | 20060608 | Coordinated                               | 370/392   |            | Huang;             |
| 20060120371 |          | convergecast for AD                       |           |            | Qingfeng           |
| A1          |          | HOC wireless networks                     |           |            | et al.             |
| US          | 20060608 | Liquid-crystal display                    | 362/561   | 362/330;   | Lin; Feng-         |
| 20060120103 |          | apparatus and backlight                   |           | 362/559;   | Li                 |
| A1          |          | module thereof                            |           | 362/560    |                    |
| US          | 20060608 | Skew tolerant high-                       | 327/3     |            | Lin; Feng          |
| 20060119396 |          | speed digital phase                       |           |            |                    |
| A1          | 20060511 | detector                                  | 254/121   |            | TT                 |
| US          | 20060511 | Infrared thermometer                      | 374/121   |            | Huang;             |
| 20060098709 |          |   |           |            | Horng-<br>Tsann et |
| A1          |          |   |           |            | al.                |
| US          | 20060420 | Methods and                               | 369/44.29 | 369/44.27; | Lin; Feng-         |
| 20060083126 | 20000420 | apparatuses of half-                      | 307/44.27 | 369/53.28  | Fu et al.          |
| A1          |          | wave layer jump                           | _         | 303/33.20  |                    |
| 711         |          | control for optical disk                  |           |            |                    |
|             |          | drive                                     |           |            | ·                  |
| US          | 20060420 | Measure-controlled                        | 327/142   |            | Lin; Feng          |
| 20060082401 | •        | delay circuits with                       |           |            |                    |
| A1          |          | reduced phase error                       |           |            |                    |
| US          | 20060323 | Interleaved delay line                    | 365/194   |            | Lin; Feng          |
| 20060062058 |          | for phase locked and                      |           |            |                    |
| A1          |          | delay locked loops                        |           |            |                    |
| US          | 20060316 | Method and apparatus                      | 600/447   |            | Lin; Feng          |
| 20060058670 |          | for ultrasound spatial                    |           |            | et al.             |
| A1          |          | compound imaging                          |           |            |                    |
|             |          | with adjustable                           |           |            |                    |
| 110         | 20060216 | aperture controls                         | 501/62    | 501/70     | Tim. Fara          |
| US          | 20060316 | Method for                                | 501/63    | 501/70;    | Lin; Feng<br>Huei  |
| 20060058172 |          | manufacturing DP-<br>bioglass composition |           | 501/73     | nuel               |
| A1          |          | for use in dental                         |           |            |                    |
|             |          | fracture repair                           | *         |            |                    |
| US          | 20060302 | Mult-interface auto-                      | 713/300   |            | Lo; Hai            |
| 20060047982 | 20000302 | switch circuit and                        | 715/500   |            | Cha et al.         |
| A1          |          | memory device with                        |           |            |                    |
| 111         |          | dual interface auto-                      |           |            |                    |
|             |          | switch circuit                            |           |            |                    |
|             | L        |   | L         | <u> </u>   | <del>-1</del>      |

| US<br>20060047861<br>A1 | 20060302 | Method for soft configuring communication protocols  | 710/11  | Hung;<br>Jeng Shin<br>et al.             |
|-------------------------|----------|--|---------|--|
| US<br>20060047860<br>A1 | 20060302 | Flash memory device<br>with a plurality of<br>protocols and a method<br>for controlling the flash<br>memory device | 710/11  | Lin; Feng<br>Hsi                         |
| US<br>20060044891<br>A1 | 20060302 | Memory system and<br>method for strobing<br>data, command and<br>address signals                                   | 365/193 | Lin; Feng et al.                         |
| US<br>20060033542<br>A1 | 20060216 | Fast-locking digital phase locked loop   | 327/156 | Lin; Feng et al.                         |
| US<br>20060030776<br>A1 | 20060209 | Range dependent<br>weighting for spatial<br>compound imaging   | 600/437 | Washburn;<br>Michael<br>Joseph et<br>al. |
| US<br>20060028905<br>A1 | 20060209 | Method and apparatus<br>for initialization of read<br>latency tracking circuit<br>in high-speed DRAM               | 365/233 | Johnson;<br>James<br>Brian et<br>al.     |
| US<br>20060017480<br>A1 | 20060126 | Method and apparatus<br>to set a tuning range for<br>an analog delay   | 327/158 | Lin; Feng                                |
| US<br>20060012410<br>A1 | 20060119 | Measure-controlled delay circuits with reduced phase error   | 327/161 | Lin; Feng                                |
| US<br>20060007616<br>A1 | 20060112 | Power supply voltage detection circuitry and methods for use of the same   | 361/90  | Pan; Dong et al.                         |
| US<br>20050286672<br>A1 | 20051229 | Phase splitter using digital delay locked loops  | 375/376 | Lin, Feng et al.                         |
| US<br>20050285841<br>A1 | 20051229 | Video display driving method of an LCD   | 345/102 | Lin, Feng-<br>Li                         |
| US<br>20050285626<br>A1 | 20051229 | Circuits and methods<br>of temperature<br>compensation for<br>refresh oscillator                                   | 326/93  | Lin, Feng                                |
| US<br>20050285606       | 20051229 | Delay lock circuit having self-calibrating   | 324/601 | Lin, Feng                                |

| A1                      |          | loop  |           |                               |                               |
|-------------------------|----------|---|-----------|-------------------------------|-------------------------------|
| US<br>20050285605       | 20051229 | Delay lock circuit having self-calibrating  | 324/601   | 324/76.54                     | Lin, Feng                     |
| US<br>20050280479<br>A1 | 20051222 | Circuits and methods of temperature compensation for refresh oscillator                                   | 331/185   |                               | Lin, Feng                     |
| US<br>20050280456<br>A1 | 20051222 | Phase detector for reducing noise   | 327/156   | -                             | Lin, Feng                     |
| US<br>20050270893<br>A1 | 20051208 | Phase detector for all-<br>digital phase locked<br>and delay locked loops                                 | 365/233   |                               | Lin, Feng et al.              |
| US<br>20050264331<br>A1 | 20051201 | Methods and apparatus for delay circuit   | 327/161   |                               | Lin, Feng et al.              |
| US<br>20050255339<br>A1 | 20051117 | Metal air cell system   | 429/12    | 427/115;<br>429/33;<br>429/40 | Tsai,<br>Tsepin et<br>al.     |
| US<br>20050242888<br>A1 | 20051103 | Phase-locked loop<br>circuits with reduced<br>lock time   | 331/16    |                               | Lee,<br>Seong-<br>hoon et al. |
| US<br>20050240791<br>A1 | 20051027 | Interleaved delay line for phase locked and delay locked loops  | 713/401   |                               | Lin, Feng                     |
| US<br>20050225368<br>A1 | 20051013 | Phase detector for reducing noise   | 327/161   |                               | Lin, Feng                     |
| US<br>20050207586<br>A1 | 20050922 | Mobile communication earphone accommodating hearing aid with volume adjusting function and method thereof | 381/74    | 455/575.2                     | Lin, Feng-<br>Chi et al.      |
| US<br>20050190667<br>A1 | 20050901 | Long Seek control device for optical disk drive and method thereof  | 369/44.28 | 369/30.17                     | Lin, Feng-<br>Fu et al.       |
| US<br>20050184739<br>A1 | 20050825 | Delay lock circuit<br>having self-calibrating<br>loop   | 324/601   |                               | Lin, Feng                     |
| US<br>20050179172<br>A1 | 20050818 | Method for preparing porous bioceramic bone substitute  | 264/643   |                               | Lin, Feng-<br>Huei et al.     |

|                         |          | materials   |           |           |                                |
|-------------------------|----------|---|-----------|-----------|--------------------------------|
| US<br>20050158972       | 20050721 | Method for  | 438/586   |           | Lin, Feng-<br>Chuan et         |
| A1                      |          | manufacturing bit line contact structure of semiconductor memory                                    |           |           | al.                            |
| US<br>20050140407<br>A1 | 20050630 | System and method to improve the efficiency of synchronous mirror delays and delay locked loops     | 327/153   |           | Lin, Feng                      |
| US<br>20050134337<br>A1 | 20050623 | Clock signal distribution with reduced parasitic loading effects                                    | 327/158   |           | Lee,<br>Seong-<br>hoon et al.  |
| US<br>20050122153<br>A1 | 20050609 | Centralizing the lock point of a synchronous circuit  | 327/291   |           | Lin, Feng<br>Dan               |
| US<br>20050116751<br>A1 | 20050602 | Method and apparatus for improving stability and lock time for synchronous circuits                 | 327/158   |           | Lin, Feng<br>et al.            |
| US<br>20050101865<br>A1 | 20050512 | Method and apparatus for ultrasound compound imaging with combined fundamental and harmonic signals | 600/447   |           | Hao,<br>Xiaohui et<br>al.      |
| US<br>20050099880<br>A1 | 20050512 | Duty cycle distortion compensation for the data output of a memory device                           | 365/233   |           | Johnson,<br>James B.<br>et al. |
| US<br>20050083099<br>A1 | 20050421 | Capture clock<br>generator using master<br>and slave delay locked<br>loops                          | 327/295   |           | Lin, Feng                      |
| US<br>20050081626<br>A1 | 20050421 | METHOD FOR DETECTING AN UNBALANCED DISC   | 73/460    |           | Lin, Feng-<br>Fu et al.        |
| US<br>20050077975<br>A1 | 20050414 | Circuits and methods of temperature compensation for refresh oscillator                             | 331/185   |           | Lin, Feng                      |
| US<br>20050058871<br>A1 | 20050317 | Oxygen separation through hydroxide-conductive membrane   | 429/30    | 204/270   | Li, Lin-<br>Feng et al.        |
| US                      | 20050303 | Optical medium  | 250/201.5 | 369/53.22 | Lin, Feng                      |

| 20050045800             |          | judging method   |         |         | Fu et al.                   |
|-------------------------|----------|--|---------|---------|-----------------------------|
| US<br>20050024108<br>A1 | 20050203 | System and method to improve the efficiency of synchronous mirror delays and delay locked loops            | 327/158 |         | Lin, Feng                   |
| US<br>20050017810<br>A1 | 20050127 | Phase detector for reducing noise  | 331/25  |         | Lin, Feng                   |
| US<br>20040232186<br>A1 | 20041125 | Backpack with umbrella support   | 224/576 | 190/102 | Huang,<br>Qingfa et<br>al.  |
| US<br>20040223367<br>A1 | 20041111 | Phase detector for all-<br>digital phase locked<br>and delay locked loops                                  | 365/154 |         | Lin, Feng et al.            |
| US<br>20040213074<br>A1 | 20041028 | Method and apparatus<br>for establishing and<br>maintaining desired<br>read latency in high-<br>speed DRAM | 365/233 |         | Johnson,<br>Brian et<br>al. |
| US<br>20040213051<br>A1 | 20041028 | METHOD AND APPARATUS FOR IMPROVING STABILITY AND LOCK TIME FOR SYNCHRONOUS CIRCUITS                        | 365/194 |         | Lin, Feng<br>et al.         |
| US<br>20040209422<br>A1 | 20041021 | SEMICONDUCTOR DEVICE HAVING TRENCH TOP ISOLATION LAYER AND METHOD FOR FORMING THE SAME                     | 438/242 |         | Chen, Yi-<br>Nan et al.     |
| US<br>20040174185<br>A1 | 20040909 | Method and apparatus for calibrating driver impedance  | 326/30  |         | Lin, Feng et al.            |
| US<br>20040158757<br>A1 | 20040812 | Interleaved delay line for phase locked and delay locked loops   | 713/401 |         | Lin, Feng                   |
| US<br>20040130366<br>A1 | 20040708 | METHOD AND SYSTEM FOR DELAY CONTROL IN SYNCHRONIZATION   | 327/158 |         | Lin, Feng<br>et al.         |

|                       |             | CIRCUITS   |           |                     |                                |
|-----------------------|-------------|--|-----------|---------------------|--------------------------------|
| US<br>200401277<br>A1 | 20040701    | Method of ultrasonic speckle reduction using wide-band frequency compounding with tissue-generated harmonics | 600/443   |                     | Lin, Feng<br>et al.            |
| US<br>200401202<br>A1 | 20040624    | Interleaved delay line for phase locked and delay locked loops   | 365/232   |                     | Lin, Feng                      |
| US<br>200401195<br>A1 | 20040624    | Interleaved delay line for phase locked and delay locked loops   | 327/156   |                     | Lin, Feng                      |
| US<br>200401098<br>A1 | 20040610    | Surface treatment composition for soft substrates  | 424/465   |                     | Bunick,<br>Frank J. 6<br>al.   |
| US<br>200400489<br>A1 | 20040311    | Ibuprofen composition  | 514/557   |                     | Bunick,<br>Frank J. 6<br>al.   |
| US<br>200400416<br>A1 | 20040304    | Methods and apparatus for duty cycle control   | 327/175   |                     | Lin, Feng                      |
| US<br>200400334<br>A1 | 20040219    | Method for manufacturing the DP-bioglass to use in dental fracture repair via the carbon dioxide lasre       | 433/228.1 |                     | Lin, Feng<br>Huei              |
| US<br>200400137<br>A1 | 20040122    | Biodegradable ceramic for medical use  | 424/602   | 106/35;<br>501/1    | Lin, Feng<br>Huei et a         |
| US<br>200302310<br>A1 | 20031218    | Methods and apparatus for delay circuit  | 327/276   |                     | Lin, Fenget al.                |
| US<br>200302273<br>A1 | 20031211    | Method and apparatus for enabling a timing synchronization circuit   | 327/141   | ,                   | Mikhalev<br>Vladimir<br>et al. |
| US<br>200302091<br>A1 | 67 20031113 | Dental retrograde-<br>filling material<br>containing cobalt and<br>process                                   | 106/35    |                     | Lin, Feng<br>Huei et a         |
| US<br>200301542<br>A1 | 20030814    | Multi-stage email intreception method  | 709/229   |                     | Lin, Feng<br>Hui               |
| US<br>200300998       | 20030529    | SOLID GEL<br>MEMBRANE  | 429/27    | 359/270;<br>429/29; | CHEN,<br>MUGUC                 |

| Al                      |          |   |           | 429/30;<br>429/303;<br>429/33   | et al.                  |
|-------------------------|----------|---|-----------|---------------------------------|-------------------------|
| US<br>20030081473<br>A1 | 20030501 | System and method for skew compensating a clock signal and for capturing a digital signal using the skew compensated clock signal | 365/200   |                                 | Lin, Feng               |
| US<br>20030081472<br>A1 | 20030501 | System and method for skew compensating a clock signal and for capturing a digital signal using the skew compensated clock signal | 365/200   |                                 | Lin, Feng               |
| US<br>20030065439<br>A1 | 20030403 | Adaptive pid control method and system for internal combustion engine rotation speed pulsation damping                            | 701/110   | 123/352;<br>123/436;<br>701/111 | Lin, Feng<br>et al.     |
| US<br>20030054246<br>A1 | 20030320 | Zinc anode for electrochemical cells  | 429/214   | 429/229;<br>549/429;<br>549/497 | Li, Lin-<br>Feng et al. |
| US<br>20030048648<br>A1 | 20030313 | Current sense and control of DC/DC converters   | 363/65    |                                 | Lin, Feng et al.        |
| US<br>20030048643<br>A1 | 20030313 | Method and circuit for start up in a power converter  | 363/21.06 |                                 | Lin, Feng<br>et al.     |
| US<br>20030043685<br>A1 | 20030306 | SYSTEM AND METHOD FOR SKEW COMPENSATING A CLOCK SIGNAL AND FOR CAPTURING A  | 365/233   |                                 | Lin, Feng               |
|                         |          | DIGITAL SIGNAL USING THE SKEW COMPENSATED CLOCK SIGNAL  |           |                                 |                         |
| US<br>20030043615<br>A1 | 20030306 | SYSTEM AND<br>METHOD FOR SKEW<br>COMPENSATING A<br>CLOCK SIGNAL<br>AND FOR  | 365/141   |                                 | Lin, Feng               |

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|-------------|-------------|-----------------------------------|---------|-------------|--------------|
|             |             | CAPTURING A                       |         |             |              |
|             |             | DIGITAL SIGNAL                    |         |             | •            |
|             |             | USING THE SKEW                    |         |             |              |
|             |             | COMPENSATED                       |         |             |              |
|             |             | CLOCK SIGNAL                      |         |             |              |
| US          | 20030220    | Method to improve the             | 327/158 |             | Lin, Feng    |
| 20030034815 |             | efficiency of                     |         |             | ·            |
| A1          |             | synchronous mirror                |         |             |              |
|             |             | delays and delay                  |         |             |              |
|             |             | locked loops                      |         |             |              |
| US          | 20030130    | Solid gel membrane                | 429/29  | 429/144;    | Chen,        |
| 20030022047 | 20030130    | Source Box mornormine             | 123,23  | 429/206;    | Muguo et     |
| A1          |             |                                   |         | 429/229;    | al.          |
|             |             |                                   |         | 429/27;     | ui.          |
|             | ·           | •                                 |         | 429/303     |              |
| US          | 20021219    | Methods for identifying           | 514/1   | 435/18      | Chen, Lin-   |
| 1           | 20021219    | modulators of NF-KB               | J14/1   | 455/10      | feng et al.  |
| 20020193284 | ļ           |                                   |         |             | icing ct ai. |
| A1          | 20021205    | activity                          | 327/158 |             | Baker, R.    |
|             | 20021205    | Digital dual-loop DLL             | 32//138 |             | Jacob et     |
| 20020180501 |             | design using coarse and           |         |             |              |
| A1          |             | fine loops                        | 075/076 |             | al.          |
| 1           | 20021121    | Phase splitter using              | 375/376 |             | Lin, Feng    |
| 20020172314 |             | digital delay locked              |         |             | et al.       |
| A1          |             | loops                             |         |             |              |
| I I         | 20021024    | Capture clock                     | 713/401 | · .         | Lin, Feng    |
| 20020157031 |             | generator using master            |         |             |              |
| A1          |             | and slave delay locked            |         |             |              |
|             |             | loops                             |         | •           |              |
| US          | 20021003    | Refuelable metal air              | 429/27  | 429/212;    | Ma,          |
| 20020142203 |             | electrochemical cell              |         | 429/246     | Fuyuan et    |
| A1          |             | and refuelabel anode              |         |             | al.          |
|             |             | structure for                     |         |             |              |
|             |             | electrochemical cells             |         |             |              |
| US          | 20021003    | Fibrous electrode for a           | 429/27  | 429/217;    | Li, Lin-     |
| 20020142202 |             | metal air                         |         | 429/218.1;  | Feng et al.  |
| A1          |             | electrochemical cell              |         | 429/221;    |              |
|             |             |                                   |         | 429/229;    |              |
|             |             | +                                 |         | 429/231.6;  |              |
|             |             |                                   |         | 429/231.95; |              |
| [           |             |                                   |         | 429/232;    |              |
| •           |             |                                   |         | 429/40      |              |
| US          | 20020919    | Duty cycle distortion             | 713/400 |             | Johnson,     |
| 20020133731 |             | compensation for the              |         | •           | James B.     |
|             | 1           |                                   |         |             | i            |
| A1          |             | 1 -                               |         |             | et al.       |
| A1          |             | data output of a<br>memory device |         |             | et al.       |

| 20020102465 |          | separator in            |           | 429/27;      | Muguo et    |
|-------------|----------|-------------------------|-----------|--------------|-------------|
| Al          |          | electrochemical cells   |           | 429/9        | al.         |
| US          | 20020711 | Multi-chip              | 257/678   | 257/E23.039; | Liao,       |
| 20020089048 |          | semiconductor package   |           | 257/E23.042; | Kuang-Ho    |
| Al          |          | structure               |           | 257/E23.052  | et al.      |
| US          | 20020620 | MULTI-CHIP              | 257/686   | 257/724;     | Liao,       |
| 20020074638 | 20020020 | SEMICONDUCTOR           |           | 257/E23.039; | Kuang-Ho    |
| A1          |          | PACKAGE                 |           | 257/E23.042; | et al.      |
|             |          | STRUCTURE               |           | 257/E23.052  |             |
| US          | 20011018 | Oxygen separation       | 204/252   | 204/296;     | Li, Lin-    |
| 20010030127 |          | through hydroxide-      |           | 205/633;     | Feng et al. |
| Al          |          | conductive membrane     |           | 521/27       |             |
| US          | 20010927 | Crossbar integrated     | 710/317   |              | Wang,       |
| 20010025332 | 20010727 | circuit with parallel   | 710/51/   | ٠            | Yuanlong    |
| A1          |          | channels for a          |           |              | et al.      |
|             |          | communication device    |           |              | l or ax.    |
| US 7126874  | 20061024 | Memory system and       | 365/233   | 365/189.05;  | Lin; Feng   |
| B2          | 20001021 | method for strobing     | 2021233   | 365/193;     | et al.      |
| D2          |          | data, command and       |           | 365/230.01;  |             |
|             |          | address signals         |           | 365/230.08   |             |
| US 7123525  | 20061017 | Phase detector for all- | 365/194   | 365/189.07;  | Lin; Feng   |
| B2          | 20001017 | digital phase locked    | 303/17:   | 365/233      | et al.      |
| DZ          |          | and delay locked loops  |           | 303/233      | or un.      |
| US 7109807  | 20060919 | Phase detector for      | 331/25    | 327/161;     | Lin; Feng   |
| B2          | 20000313 | reducing noise          |           | 327/7        |             |
| US 7106603  | 20060912 | Switch-mode self-       | 363/21.08 | 363/21.16;   | Lin; Feng-  |
| B1          | 20000312 | coupling auxiliary      |           | 363/97       | Hsu et al.  |
|             |          | power device            |           |              |             |
| US 7103791  | 20060905 | Interleaved delay line  | 713/401   | 327/147;     | Lin; Feng   |
| B2          |          | for phase locked and    |           | 327/149;     |             |
|             |          | delay locked loops      |           | 327/182;     |             |
|             |          | 1                       |           | 327/183;     |             |
|             |          |                         |           | 365/194;     |             |
|             |          |                         |           | 365/233;     |             |
|             |          |                         |           | 375/371;     |             |
| -           |          |                         |           | 375/373;     |             |
|             |          |                         |           | 711/100;     | ľ           |
|             |          | ,                       |           | 711/105;     |             |
|             |          |                         |           | 711/167;     |             |
|             |          |                         |           | 711/170;     | 1           |
|             |          |                         |           | 713/400;     |             |
|             |          |                         |           | 713/500;     |             |
|             |          |                         |           | 713/503      |             |
| US 7102361  | 20060905 | Delay lock circuit      | 324/601   | 324/76.54    | Lin; Feng   |
| B2          |          | having self-calibrating |           |              |             |
|             |          | loop                    |           |              |             |
| <u> </u>    | L        | 1 100P                  | L         | L            |             |

| US 7098714<br>B2 | 20060829 | Centralizing the lock point of a synchronous circuit   | 327/291   | 327/158   | Lin; Feng<br>Dan                     |
|------------------|----------|--|-----------|---|--------------------------------------|
| US 7081343<br>B2 | 20060725 | Methods for identifying modulators of NF-KB activity   | 435/7.1   | 435/18;<br>435/29   | Chen; Linfeng et al.                 |
| US 7078053<br>B2 | 20060718 | Ibuprofen composition  | 424/441   |   | Bunick;<br>Frank J. et<br>al.        |
| US 7065001<br>B2 | 20060620 | Method and apparatus<br>for initialization of read<br>latency tracking circuit<br>in high-speed DRAM | 365/233 . | 365/194;<br>365/236   | Johnson;<br>James<br>Brian et<br>al. |
| US 7058688<br>B2 | 20060606 | Multi-stage email interception method  | 709/206   | 707/3; 707/6;<br>709/207  | Lin; Feng-<br>Hui                    |
| US 7028208<br>B2 | 20060411 | Duty cycle distortion compensation for the data output of a memory device                            | 713/401   | 327/158;<br>327/159;<br>365/233;<br>713/400;<br>713/500;<br>713/501   | Johnson;<br>James B.<br>et al.       |
| US 7028134<br>B2 | 20060411 | Crossbar integrated circuit with parallel channels for a communication device                        | 710/317   | 370/360;<br>710/316   | Wang;<br>Yuanlong<br>et al.          |
| US 7020794<br>B2 | 20060328 | Interleaved delay line for phase locked and delay locked loops                                       | 713/401   | 307/141;<br>307/409;<br>327/149;<br>327/152;<br>327/153;<br>327/158;<br>327/182;<br>365/194;<br>365/233;<br>370/509;<br>370/517;<br>370/519;<br>713/400;<br>713/500;<br>713/503;<br>713/600 | Lin; Feng                            |
| US 7009407<br>B2 | 20060307 | Delay lock circuit having self-calibrating loop  | 324/601   | 324/76.54   | Lin; Feng                            |
| US D515935<br>S  | 20060228 | Cosmetics container  | D9/663    | D9/558;<br>D9/661   | Lin; Feng-                           |

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| US 6992534<br>B2 | 20060131 | Circuits and methods of temperature compensation for refresh oscillator   | 331/176 | 331/177R;<br>331/177V;<br>331/186;<br>331/57  | Lin; Feng                       |
|------------------|----------|---|---------|---|---------------------------------|
| US 6987701<br>B2 | 20060117 | Phase detector for all-<br>digital phase locked<br>and delay locked loops   | 365/194 | 327/156;<br>327/158;<br>365/233   | Lin; Feng<br>et al.             |
| US 6950487<br>B2 | 20050927 | Phase splitter using digital delay locked loops   | 375/376 |   | Lin; Feng et al.                |
| US 6940328<br>B2 | 20050906 | Methods and apparatus for duty cycle control  | 327/175 | 327/172   | Lin; Feng                       |
| US 6930955<br>B2 | 20050816 | Method and apparatus<br>for establishing and<br>maintaining desired<br>read latency in high-<br>speed DRAM  | 365/233 | 365/194;<br>365/236   | Johnson;<br>Brian et<br>al.     |
| US 6930525<br>B2 | 20050816 | Methods and apparatus for delay circuit   | 327/161 | 327/276   | Lin; Feng et al.                |
| US 6912666<br>B2 | 20050628 | Interleaved delay line<br>for phase locked and<br>delay locked loops  | 713/401 | 711/100;<br>711/105;<br>711/167;<br>711/170;<br>713/400;<br>713/500;<br>713/503;<br>713/600 | Lin; Feng                       |
| US 6895522<br>B2 | 20050517 | Method and apparatus for compensating duty cycle distortion in a data output signal from a memory device by delaying and distorting a reference clock | 713/401 | 365/233;<br>713/400;<br>713/600;<br>713/601   | Johnson;<br>James B.<br>et al.  |
| US 6891415<br>B2 | 20050510 | Method and apparatus for enabling a timing synchronization circuit  | 327/158 | 327/161   | Mikhalev;<br>Vladimir<br>et al. |
| US 6872619<br>B2 | 20050329 | Semiconductor device<br>having trench top<br>isolation layer and<br>method for forming the<br>same  | 438/242 | 257/300;<br>257/302;<br>438/243   | Chen; Yi-<br>Nan et al.         |
| US 6868504<br>B1 | 20050315 | Interleaved delay line<br>for phase locked and<br>delay locked loops  | 713/500 | 307/141;<br>307/409;<br>365/194;<br>365/233;  | Lin; Feng                       |

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|------------|----------|-------------------------|---------|--------------|-----------|
|            |          |                         |         | 370/508;     |           |
| •          |          |                         |         | 370/517;     |           |
|            |          |                         |         | 370/519;     |           |
|            |          |                         |         | 704/502;     | ,         |
|            |          | ·                       |         | 713/401;     |           |
|            |          |                         |         | 713/503      |           |
| US 6845459 | 20050118 | System and method to    | 713/401 | 327/147;     | Lin; Feng |
| B2         |          | provide tight locking   |         | 327/149;     |           |
|            |          | for DLL and PLL with    |         | 327/157;     |           |
|            |          | large range, and        |         | 327/158;     |           |
|            |          | dynamic tracking of     |         | 327/161;     |           |
|            | l '      | PVT variations using    |         | 711/100;     |           |
|            |          | interleaved delay lines | -       | 711/105;     | .         |
|            |          | •                       |         | 713/400;     |           |
|            |          |                         |         | 713/500;     |           |
|            |          |                         |         | 713/503      |           |
| US 6845458 | 20050118 | System and method of    | 713/401 | 327/147;     | Lin; Feng |
| B2         |          | operation of DLL and    |         | 327/149;     |           |
|            |          | PLL to provide tight    |         | 327/157;     |           |
|            |          | locking with large      |         | 327/158;     | ļ         |
|            |          | range, and dynamic      |         | 327/161;     |           |
|            |          | tracking of PVT         |         | 713/400;     |           |
|            |          | variations using        |         | 713/500;     |           |
|            |          | interleaved delay lines |         | 713/503      |           |
| US 6844616 | 20050118 | Multi-chip              | 257/676 | 257/E23.039; | Liao;     |
| B2         |          | semiconductor package   |         | 257/E23.042; | Kuang-Ho  |
|            |          | structure               |         | 257/E23.052  | et al.    |
| US 6839860 | 20050104 | Capture clock           | 713/401 | 713/503      | Lin; Feng |
| B2         |          | generator using master  |         |              |           |
|            |          | and slave delay locked  |         |              |           |
|            |          | loops                   |         |              |           |
| US 6839301 | 20050104 | Method and apparatus    | 365/233 | 327/141;     | Lin; Feng |
| B2         |          | for improving stability |         | 327/142;     | et al.    |
|            | ·        | and lock time for       |         | 327/154;     |           |
|            |          | synchronous circuits    |         | 327/155;     |           |
|            |          |                         |         | 327/156;     |           |
|            |          | ,                       |         | 327/158;     |           |
|            |          | `                       |         | 327/159;     |           |
|            |          |                         |         | 365/194      |           |
| US 6836166 | 20041228 | Method and system for   | 327/158 | 327/161;     | Lin; Feng |
| B2         |          | delay control in        |         | 327/276;     | et al.    |
|            |          | synchronization         |         | 327/299      |           |
|            |          | circuits                |         |              |           |
| US 6827685 | 20041207 | Method of ultrasonic    | 600/437 |              | Lin; Feng |
| B2         |          | speckle reduction using |         |              | et al.    |
|            |          | wide-band frequency     |         |              |           |
|            |          |                         |         | -            |           |

|                  |          | compounding with tissue-generated harmonics   |           |   |                              |
|------------------|----------|---|-----------|---|------------------------------|
| US 6812753<br>B2 | 20041102 | System and method for<br>skew compensating a<br>clock signal and for<br>capturing a digital<br>signal using the skew<br>compensated clock<br>signal | 327/155   | 327/156;<br>327/158;<br>327/159   | Lin; Feng                    |
| US 6798259<br>B2 | 20040928 | System and method to improve the efficiency of synchronous mirror delays and delay locked loops   | 327/153   | 327/299;<br>365/233   | Lin; Feng                    |
| US 6794069<br>B1 | 20040921 | Fuel cell support and electrical interconnector   | 429/17    | 429/123;<br>429/152;<br>429/9   | Faris;<br>Sadeg M.<br>et al. |
| US 6779126<br>B1 | 20040817 | Phase detector for all-<br>digital phase locked<br>and delay locked loops   | 713/500   | 327/147;<br>327/156;<br>327/160;<br>327/166;<br>713/400;<br>713/502;<br>713/503;<br>713/600 | Lin; Feng et al.             |
| US 6774690<br>B2 | 20040810 | Digital dual-loop DLL design using coarse and fine loops  | 327/158   | 327/156;<br>327/278;<br>375/376   | Baker; R.<br>Jacob et<br>al. |
| US 6767663<br>B2 | 20040727 | Oxygen separation through hydroxide-conductive membrane   | 429/33    | 429/19;<br>429/303;<br>521/27   | Li; Lin-<br>Feng et al.      |
| US 6762974<br>B1 | 20040713 | Method and apparatus<br>for establishing and<br>maintaining desired<br>read latency in high-<br>speed DRAM  | 365/233   | 365/194;<br>365/236   | Johnson;<br>Brian et<br>al.  |
| US 6760235<br>B2 | 20040706 | Soft start for a synchronous rectifier in a power converter   | 363/21.06 | 363/49  | Lin; Feng et al.             |
| US 6759882<br>B2 | 20040706 | System and method for skew compensating a clock signal and for capturing a digital signal using the skew compensated clock                          | 327/155   | 327/156;<br>327/158;<br>327/159   | Lin; Feng                    |

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|------------------|----------|---|---------|--|-------------------------------|
| US 6699189<br>B1 | 20040302 | Ultrasound distortion compensation using blind system identification  | 600/437 |  | Lin; Feng<br>et al.           |
| US 6687185<br>B1 | 20040203 | Method and apparatus for setting and compensating read latency in a high speed DRAM   | 365/233 | 327/147;<br>327/156;<br>327/158;<br>365/194                  | Keeth;<br>Brent et<br>al.     |
| US 6653003<br>B1 | 20031125 | Fuel cell support and electrical interconnector   | 429/17  | 429/123;<br>429/152;<br>429/18;<br>429/27;<br>429/9          | Tsai;<br>Tsepin et<br>al.     |
| US 6627214<br>B1 | 20030930 | Ibuprofen composition   | 424/441 | 424/439;<br>424/469;<br>424/484;<br>514/784;<br>514/974      | Bunick;<br>Frank J. et<br>al. |
| US 6618283<br>B2 | 20030909 | System and method for skew compensating a clock signal and for capturing a digital signal using the skew compensated clock signal | 365/141 | 365/155;<br>365/158;<br>365/161                              | Lin; Feng                     |
| US 6611475<br>B2 | 20030826 | System and method for skew compensating a clock signal and for capturing a digital signal using the skew compensated clock signal | 365/233 | 365/194  | Lin; Feng                     |
| US 6605391<br>B2 | 20030812 | Solid gel membrane  | 429/303 | 429/29   | Chen;<br>Muguo et<br>al.      |
| US 6591808<br>B2 | 20030715 | Adaptive PID control method and system for internal combustion engine rotation speed pulsation damping                            | 123/352 | 123/192.1;<br>123/339.21;<br>123/436;<br>701/110;<br>701/111 | Lin; Feng<br>et al.           |
| US 6574124<br>B2 | 20030603 | Plural power converters with individual conditioned error signals shared on a   | 363/65  | 363/72;<br>363/97  | Lin; Feng et al.              |

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|                  |          | current sharing bus  |           |  |                              |
|------------------|----------|--|-----------|--|------------------------------|
| US 6541159<br>B1 | 20030401 | Oxygen separation through hydroxide-   | 429/236   | 204/295;<br>205/629;   | Li; Lin-<br>Feng et al.      |
| DI               |          | conductive membrane  |           | 429/235  |                              |
| US 6469988<br>B1 | 20021022 | Low-level circuit implementation of signal flow graphs for real-time signal processing of high-speed digital signals                         | 370/289   | 370/296;<br>375/220;<br>375/346  | Yang;<br>Kewei et<br>al.     |
| US 6458617<br>B1 | 20021001 | Multi-chip<br>semiconductor package<br>structure   | 438/51    | 257/E23.039;<br>257/E23.042;<br>257/E23.052;<br>438/108;<br>438/123;<br>438/55 | Liao;<br>Kuang-Ho<br>et al.  |
| US 6445231<br>B1 | 20020903 | Digital dual-loop DLL design using coarse and fine loops   | 327/158   | 327/278;<br>375/376  | Baker; R.<br>Jacob et<br>al. |
| US 6418537<br>B1 | 20020709 | Accurate timing calibration for each of multiple high-speed clocked receivers using a single DLL   | 713/400   | 710/60;<br>713/503   | Yang;<br>Kewei et<br>al.     |
| US 6358651<br>B1 | 20020319 | Solid gel membrane separator in rechargeable electrochemical cells   | 429/303   | 29/623.1   | Chen;<br>Muguo et<br>al.     |
| US 6338752<br>B1 | 20020115 | .alphaTCP/HAP<br>biphasic cement and its<br>preparing process  | 106/35    | 106/670;<br>423/305;<br>423/311;<br>423/315;<br>501/1                          | Lin; Feng-<br>Huei et al.    |
| US 6310410<br>B1 | 20011030 | Method and apparatus for reducing source voltage deviation in hot plug applications  | 307/119   | 307/135;<br>307/147;<br>361/58   | Lin; Feng et al.             |
| US 6226696<br>B1 | 20010501 | Programmable circuit for sensing computer pointing devices to sink a different amount of current depending on the requirements of the device | 710/8     | 345/161;<br>345/163;<br>710/14   | Lin; Feng-<br>Chen et al.    |
| US 6224629<br>B1 | 20010501 | Bone substitute composition and  | 623/16.11 | 623/17.11  | Lin; Feng-<br>Huei et al.    |

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|------------------|----------|--|-----------|---|---|
|                  |          | process of preparation thereof   |           | •   |   |
| US 6181577<br>B1 | 20010130 | Auxiliary bias circuit for a power supply and a method of operation thereof              | 363/21.01 | 363/141   | Chen;<br>Shiaw-<br>Jong Steve<br>et al. |
| US 6136030<br>A  | 20001024 | Process for preparing porous bioceramic materials  | 128/898   | 514/324;<br>523/113;<br>623/919                               | Lin; Feng-<br>Huei et al.               |
| US 6085555<br>A  | 20000711 | Palm-top fabric leading edge detector  | 68/13R    | 68/213;<br>68/235R  | Wu; Jong-<br>Fu et al.                  |
| US 6081436<br>A  | 20000627 | Multi-output power supply voltage sensing  | 363/65    |   | Lin; Feng                               |
| US 6061260<br>A  | 20000509 | Board mounted power supply having an auxiliary output                                    | 363/141   | 257/E23.099;<br>361/687                                       | Chen;<br>Shiaw-<br>Jong Steve<br>et al. |
| US 6055169<br>A  | 20000425 | Current mode control circuit for paralleled power supply and method of operation thereof | 363/65    | 363/71  | Bowman;<br>Wayne C.<br>et al.           |
| US 6005674<br>A  | 19991221 | System architecture for multiple input/output devices                                    | 358/437   | 358/434;<br>358/468;<br>379/100.01;<br>379/100.12;<br>710/260 | Lin; Feng                               |
| US 5881064<br>A  | 19990309 | Packet-switched data<br>network and method of<br>operation                               | 370/389   | 358/426.1   | Lin; Feng<br>et al.                     |
| US 5730806<br>A  | 19980324 | Gas-liquid supersonic cleaning and cleaning verification spray system                    | 134/22.12 | 134/102.1;<br>134/102.2;<br>134/113;<br>134/36                | Caimi;<br>Raoul E.<br>B. et al.         |
| US 5687589<br>A  | 19971118 | Batch dyeing system with automatic chemical-dosing and pH control system                 | 68/12.07  | 68/207  | Yao; Shin-<br>Chuan et<br>al.           |
| US 5546388<br>A  | 19960813 | Packet-switched facsimile network and method of operation                                | 370/389   | 359/425   | Lin; Feng                               |
| US 5364024<br>A  | 19941115 | Application independent, portable room temperature and humidity controller               | 236/44C   | 165/229;<br>62/176.6  | Lin; Feng                               |
| US D337392       | 19930713 | Table lamp   | D26/106   |   | Lin; Feng-                              |

| S               |          |  |           |  | Jye                           |
|-----------------|----------|--|-----------|--|-------------------------------|
| US D336534<br>S | 19930615 | Lamp head  | D26/63    | D26/128  | Lin; Feng-<br>Jye             |
| US 5200254<br>A | 19930406 | Receptor sheet<br>manifolds for thermal<br>mass transfer imaging | 428/32.39 | 428/323;<br>428/331;<br>428/913;<br>428/914                                      | Henry;<br>Robert M.<br>et al. |
| US D330266<br>S | 19921013 | Adjustable lamp  | D26/65    |  | Lin; Feng-<br>Jye             |
| US 4813463<br>A | 19890321 | Instantly applicable correction fluid container structure        | 141/351   | 141/346;<br>222/518;<br>401/260;<br>401/264                                      | Lin; Feng-<br>Yu              |
| US 4202472<br>A | 19800513 | Dispenser for insecticide or the like                            | 222/187   | 206/205;<br>206/222;<br>206/532;<br>383/40                                       | Lin; Feng                     |
| US 4105144<br>A | 19780808 | Dispenser for insect or vermin poison                            | 222/187   | 206/205;<br>206/222;<br>206/532;<br>222/541.9;<br>383/205;<br>383/40;<br>428/907 | Lin; Feng                     |